#3 Flillon 5/15/0/ Sheet 1 of 2

Form PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DKT. NO. 501.39813X00

SERIAL NO.

APPLICANT

T. YAZAKI ET AL.

March 20, 2001

FILING DATE

GROUP

(Use several sheets if necessary)

INFORMATION DISCLOSURE STATEMENT

BY APPLICANT

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
AA	5,959,991	09/28/99	Hatono et al.			10/16/96
AB	5,546,389	08/13/96	Wippenbeck et al.			07/12/94
AC						
AD						ļ
AE						_
AF						
AG						
AH						
AI					ļ	<u> </u>
AJ						
AK		<u> </u>				<u> </u>
AL						<u> </u>

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country		Subclass	Translation	
				Class		Yes	No
AM	11-55278	02/26/99	Japan				х
AN	7-95212	04/07/95	Japan				x
AO							
AP					ļ		
AQ					ļ		
AR						<u> </u>	
AS					ļ		ļ
AT					1	<u>l</u>	<u> </u>

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

v	THER DOCUMENTS (Including Figure)				
AU	Patent Abstracts of Japan 11-055278				
AV	Patent Abstracts of Japan 07-095212				
AW	The Transactions of the Institute of Electronics, Information and Communication Engineers, Vo. J83-B, No. 4, pp. 377-624, April 25, 2000				
AX	Golestani, "A Self-Clocked Fair Queueing Scheme for Broadband Applications", <i>IEEE Infocomm</i> '94, Proceedings Vol. 2, pp. v-xvi, 636-646, June 12-13 and 14-16, 1994.				
AY	S. Tsuboi et al., "Goodput Evaluation of VC Queue and FIFO Queue Methods of Implementing GFR", B Vol. J83-B, No. 4, pp. 446-456, April 2000.				
AZ					
Examiner	Date Considered				